



## General purpose transistor

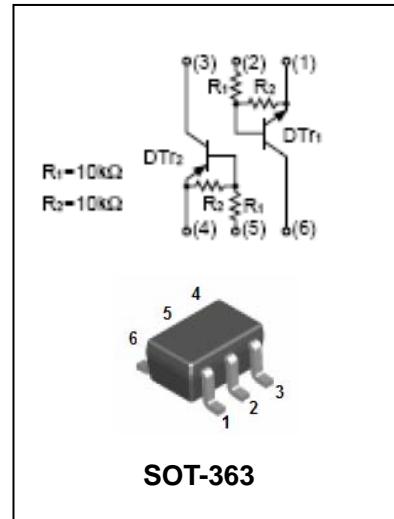
UMD3N

### FEATURES

- Both the DTA114E chip and DTC114E chip in a EMT or UMT or SMT package.
- Mounting possible with EMT3 or UMT3 or SMT3 automatic mounting machines.
- Transistor elements are independent, eliminating interference.
- Mounting cost and area can be cut in half.



Lead-free



SOT-363

### APPLICATIONS

- NPN/PNP epitaxial planar silicon transistor.

### ORDERING INFORMATION

Type No.	Marking	Package Code
UMD3N	D3	SOT-363

### MAXIMUM RATING @ Ta=25°C unless otherwise specified

Symbol	Parameter	Value	Unit
V <sub>CC</sub>	Supply Voltage	50	V
V <sub>IN</sub>	Input Voltage	-10	V
		40	
I <sub>O</sub>	Output Current	50	mA
I <sub>C</sub>	Collector Current	100	mA
P <sub>c</sub>	Power Dissipation	150	mW
T <sub>j</sub> , T <sub>stg</sub>	Junction and Storage Temperature	-55 to +150	°C



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### ELECTRICAL CHARACTERISTICS @ $T_a=25^\circ\text{C}$ unless otherwise specified

Parameter	Symbol	Test conditions	MIN	TYP	MAX	UNIT
Input Voltage	$V_{I(\text{off})}$	$V_{CC}=5\text{V}, I_O=100\mu\text{A}$	-	-	0.5	V
Input Voltage	$V_{I(\text{on})}$	$V_O=0.3\text{V}, I_O=10\text{mA}$	3	-	-	V
Output Voltage	$V_{O(\text{on})}$	$I_O=10\text{mA}, 0.5\text{mA}$	-	0.1	0.3	V
Input Current	$I_I$	$V_I=-5\text{V}$	-	-	0.88	mA
Output Current	$I_{O(\text{off})}$	$V_{CC}=50\text{V}, V_I=0\text{V}$	-	-	0.5	uA
DC Current Gain	$G_I$	$V_O=5\text{V}, I_O=5\text{mA}$	30	-	-	-
Input Resistor	$R_1(R_2)$	-	7	10	13	kΩ
Resistance Ratio	$R_2/R_1$	-	0.8	1	1.2	-
Gain-Bandwidth Product	$f_T$	$V_{CE}=10\text{V}, I_E=-5\text{mA}, f=100\text{MHz}$	-	250	-	MHz

### TYPICAL CHARACTERISTICS @ $T_a=25^\circ\text{C}$ unless otherwise specified

DTr1 (NPN)

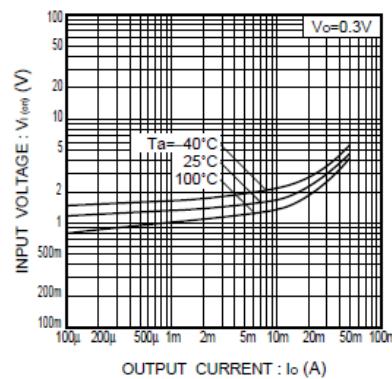


Fig.1 Input voltage vs. output current  
(ON characteristics)

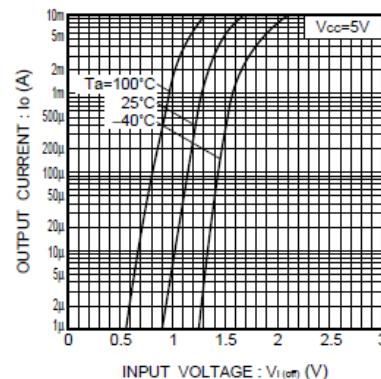


Fig.2 Output current vs. input voltage  
(OFF characteristics)

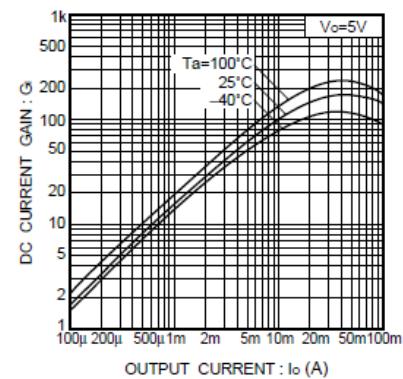


Fig.3 DC current gain vs. output current

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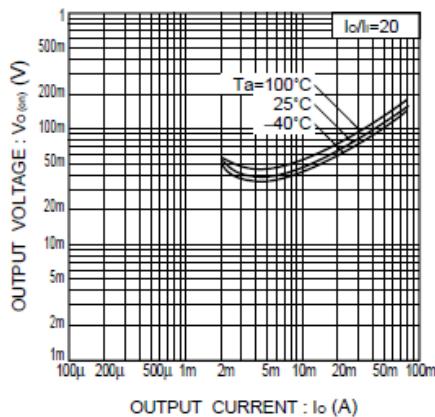


Fig.4 Output voltage vs. output current

DTr2 (PNP)

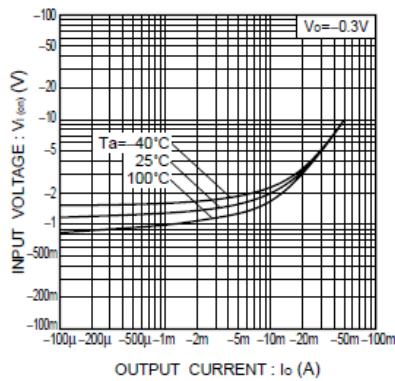


Fig.5 Input voltage vs. output current (ON characteristics)

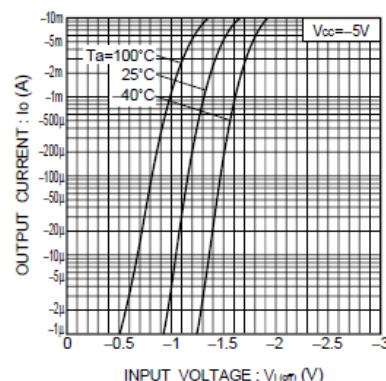


Fig.6 Output current vs. input voltage (OFF characteristics)

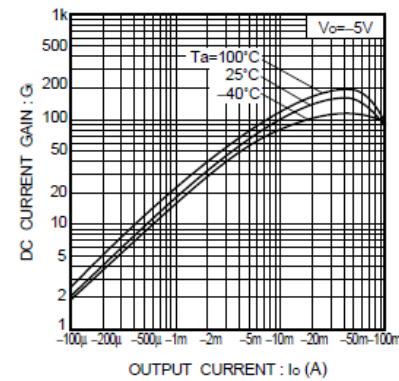


Fig.7 DC current gain vs. output current

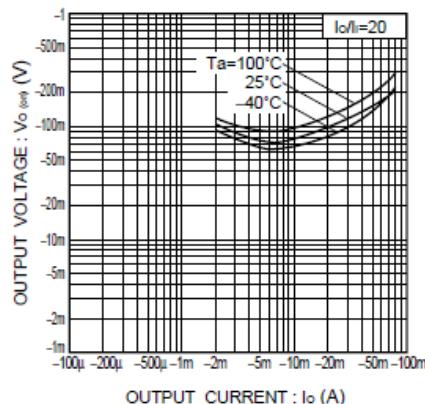


Fig.8 Output voltage vs. output current



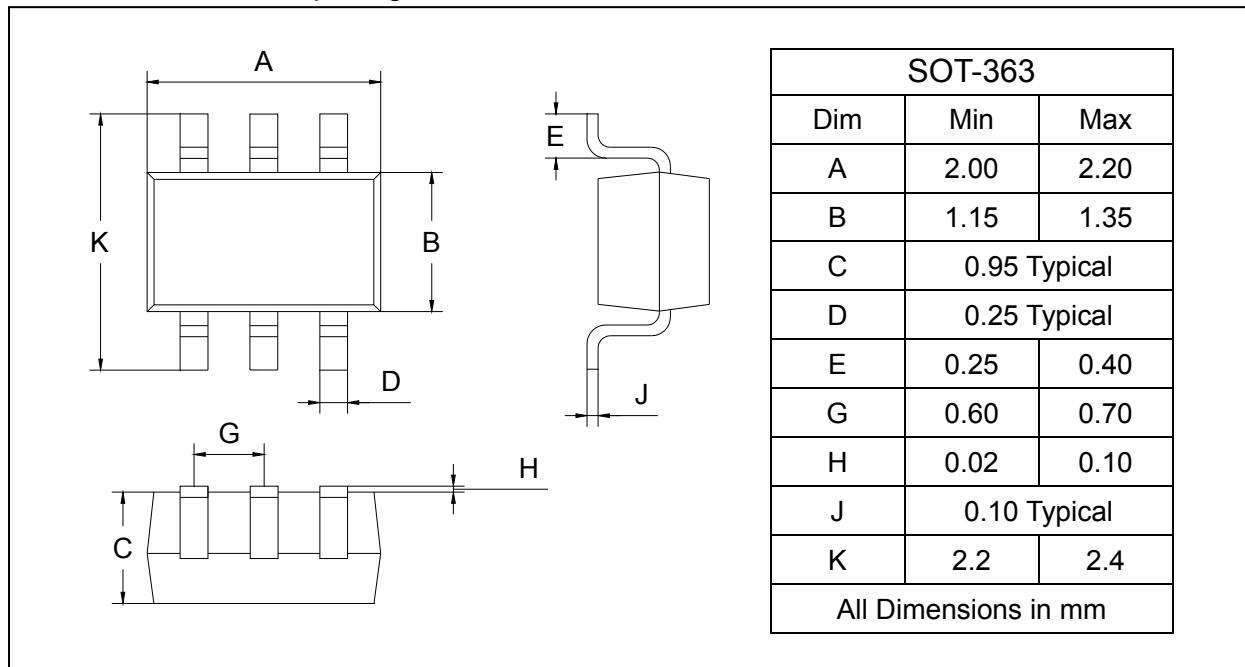
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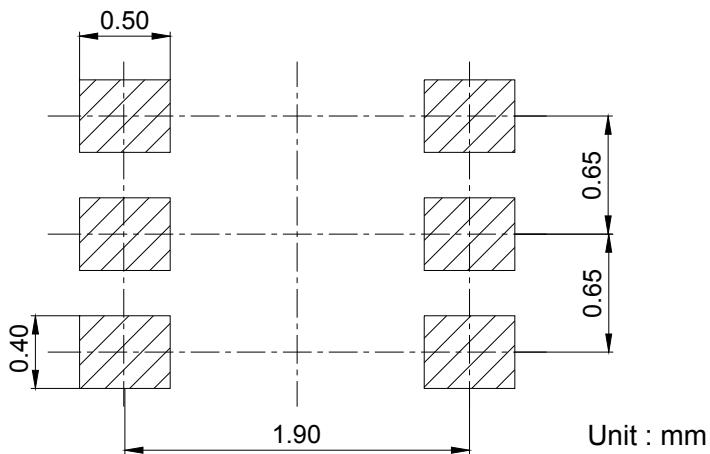
### PACKAGE OUTLINE

Plastic surface mounted package

SOT-363



### SOLDERING FOOTPRINT



### PACKAGE INFORMATION

Device	Package	Shipping
UMD3N	SOT-363	3000/Tape&Reel